

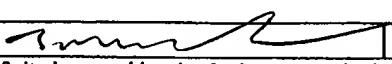
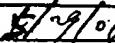
<p><b>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231</b></p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p>	<p><b>ATTY. DOCKET NO.</b> <b>MTI-31529</b></p> <hr/> <p><b>Applicant</b> <b>Ronald A. Weimer</b></p> <hr/> <p><b>Filing Date</b> <b>08/22/2001</b></p>	<p><b>Serial No.</b> <b>09/935,255</b></p> <hr/> <p><b>Confirmation No.</b> <b>1208</b></p> <hr/> <p><b>Group Art Unit</b> <b>2813</b></p>
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#### U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub- Class

#### OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial		Non-Patent Document
	C1	Wolf, Silicon Processing for the VLSI Era, Vol. 2-Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 332-335

Examiner Initials		Date Considered	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

Patent

Attorney Docket No. MTI-31529

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231	ATTY. DOCKET NO. MTI-31529	Serial No. 09/935,235
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Confirmation No. 1208
Applicant Ronald A. Weimer		Filing Date 08/22/2001
		Group Art Unit 2813

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## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub- Class
~ A1	5,563,093	10-08-96	Koda et al.	438	231
	A2 6,005,807	12-21-99	Chen	365	185.26
	A3 6,087,229	07-11-00	Aronowitz et al.	438	591
~ A4	6,323,114 B1	11-27-01	Hattangady et al.	438	591

## OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

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CI	Wolf, Silicon Processing for the VLSI Era, Vol. 2 Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 332-335

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<b>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</b> Washington, D.C. 20231	<b>ATTY. DOCKET NO.</b> <hr/> MTI-31529	<b>Serial No.</b> <hr/> 09/935,255
	<b>Applicant</b> <hr/> Ronald A. Weimer	<b>Confirmation No.</b> <hr/> 1208
	<b>Filing Date</b> <hr/> 08/22/2001	<b>Group Art Unit</b> <hr/> 2813

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